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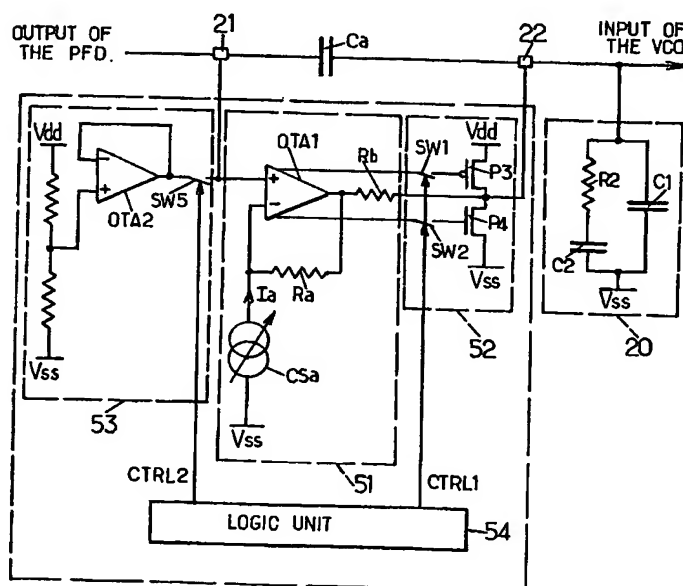
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(54) Title: VOLTAGE SHIFT CONTROL CIRCUIT FOR PLL



(57) Abstract: A PLL structure comprises a PFD, a loop filter and a VCO, as well as a voltage shift capacitor (Ca) coupling the PFD and the VCO. A voltage shift control circuit (50) is placed in parallel with the voltage shift capacitor. This circuit comprises controlled charging means (51), which are designed to charge the voltage shift capacitor according to a channel control signal. It also comprises controlled pre-charging means (52) which are designed to accelerate the charging of the voltage shift capacitor by the controlled charging means. It further comprises controlled polarization means (53), designed to ensure the polarization of the input during the pre-charging of the voltage shift capacitor.



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